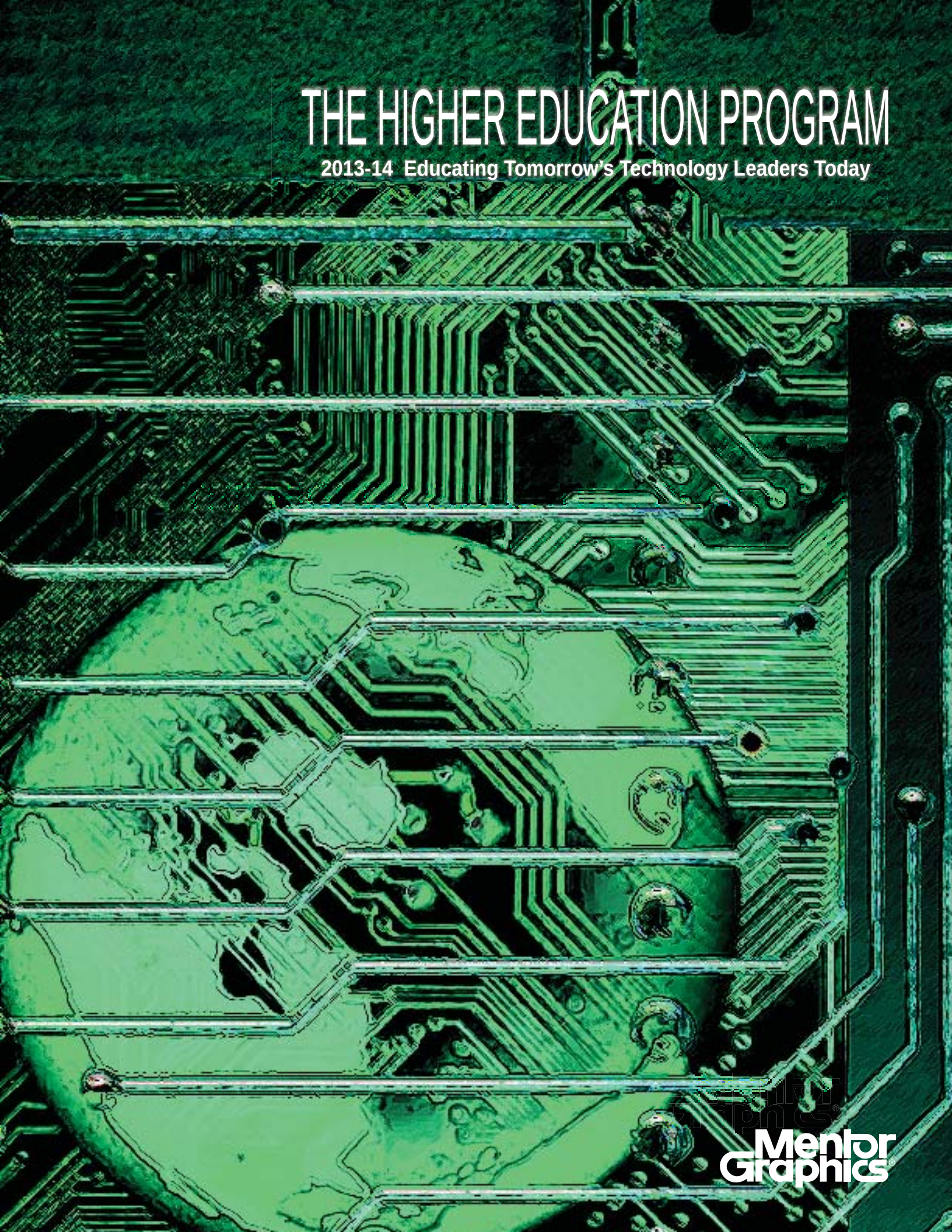
THE HIGHER EDUCATION PROGRAM

2013-14 Educating Tomorrow's Technology Leaders Today



Mentor Graphics®

Contractor/manufacturer is:
Mentor Graphics Corporation
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Contact Your Technical Writer: http://www.mentor.com/supportnet/documentation/reply_form.cfm

INTRODUCTION

Welcome to Mentor Graphics' Higher Education Program (HEP).

Mentor Graphics, a technology leader in electronic design automation (EDA) with the broadest industry portfolio of best-in-class products, founded the Higher Education Program (HEP) in 1985 to further the development of skilled engineers within the electronics industry. HEP provides schools around the globe with leading edge design tools for classroom instruction and academic research to help ensure that engineering graduates enter industry proficient in state-of-the-art tools and techniques. To date, Mentor Graphics is proud to have partnered with more than 1400 academic institutions worldwide.

In this brochure you will find details on the Mentor Graphics products available through HEP. We organize our products into a series of design packages from which you will choose based on your EDA needs. Upon admittance to the program, you will receive licenses and media for all products in the package(s) that you choose. This ensures that you receive the maximum benefit for each package's support charge and that you receive your desired products and all optional components immediately, without the need to request changes to your configuration mid-term.

For our existing members, please make sure that you check Renewing Members Only Section; each year strategic decisions are made that may necessitate adding and/or removing products from the program. To apply for or renew your annual HEP membership, please visit www.mentor.com/company/higher_ed/.

Thank you once again for your continued support. We hope you have a productive year as a member of Mentor Graphics Higher Education Program!

Sincerely,

Ian Burgess
HEP Development Director
Mentor Graphics

TABLE OF CONTENTS

DESIGN PACKAGES

For 2013-14, Mentor Graphics products available through HEP are divided into eight (8) design packages. Table 1 shows these packages and their corresponding support prices. When you order your HEP software you will select by package, and you will receive licenses and media for all products included in that package. (Support Fees are irrespective of the number of licenses you require.)

DESCRIPTION	MAIN PRODUCT COMPONENTS	US SUPPORT PRICE
IC Nanometer Design	ADiT™, Eldo®, Questa™ ADMS, Pyxis™, Calibre®, IE3D	$500
Design, Verification & Test	Vista™, ReqTracer™, Questa (including ModelSim®), Precision Synthesis, Leonardo Spectrum™ ASIC, Tessent™ Silicon Test, Questa Codelink, SystemVision™, Bridgepoint®	$500
PCB Expedition®	Expedition Pinnacle™ Layout,, I/O Designer™ FPGA Integration, Fablink™ XE Pro, HyperLynx®, Quiet™ Expert , Eldo, DxDesigner®, IE3D	$500
PCB PADS®	DxDesigner, PADS Logic, PADS Layout, PADS Autorouter, HyperLynx®	$500
Cabling & Harness	Capital® Design, Views, Analysis and Harness; Vesys®; SystemVision	$500
Mechanical Analysis	FloTHERM™, FloTHERM PCB, FloTHERM PACK FloVENT™, FloEFD™, FloEFD Electronic Cooling & Adv CFD, Flowmaster®	$500
PCB Board Station	This package is only available as a renewal.	
Embedded SW Development	This package is only available as a renewal.	

**For international pricing and product options, please contact your regional HEP representative.

IC NANOMETER DESIGN

The IC Nanometer Design package provides a complete environment for the design, capture, layout and verification of analog, digital and mixed-signal integrated circuits. This package includes all products that incorporate the IC Nanometer Design platform:

The **Pyxis** suite of IC design tools

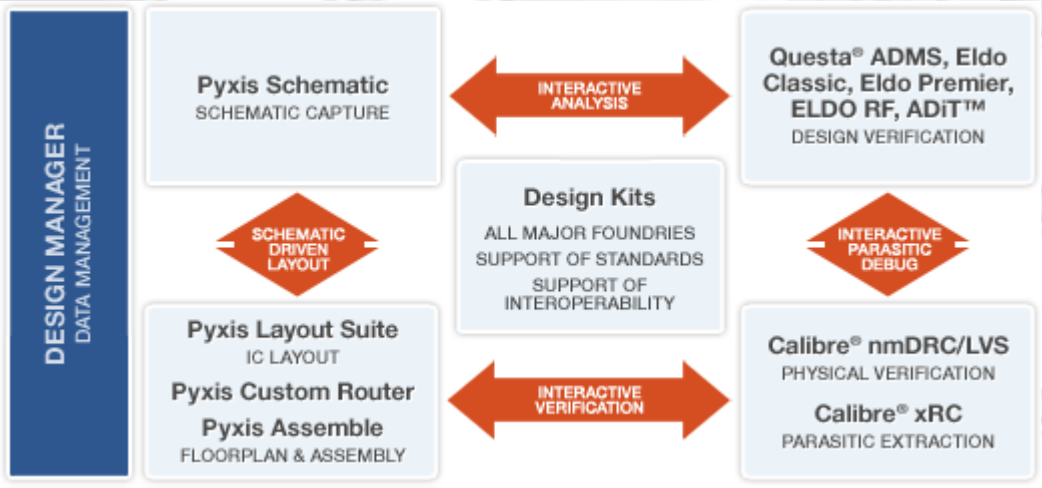
- Schematic capture, netlisting, simulation setup and results viewing.
- Physical layout
- Editing, schematic-driven layout, and top-level floorplanning and routing.

The **Questa ADMS** analog and mixed signal verification suite

- Questa ADMS and Questa AMDS RF - A language-neutral, mixed-signal simulator that enables top-down design and bottom-up verification of multi-million gate analog/mixed-signal SoC designs.
- Eldo and Eldo RF - An analog simulator offering numerous simulation and modeling options that deliver high-performance and high-speed simulation with the accuracy required by the user.
- ADiT™ - A fast-SPICE simulator built specifically for analog and mixed-signal applications.

The **Calibre** product line for physical verification and design for manufacturability of deep sub micron integrated circuits

- Calibre - The industry standard platform for physical verification, offering superior performance and capacity for both flat and hierarchical algorithms.
- Calibre xRC - Accurate transistor-level, gate-level and hierarchical parasitic extraction.



IC NANOMETER DESIGN TOOLS

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Analog-Mixed Signal		Platform
227863	ADiT TURBO Ap SW	
250218	Questa ADMS Core MixedHDL Bnd SW	
212235	Questa ADMS RF Op SW	LINUX
130007	Artist Link Ap SW	SUN
204979	Eldo RF Op SW	

IC Flow/Pyxis		
233852	ICanalyst CC Ap SW	
221498	IC EDIF200 Netlist Read Stn SW	
221494	IC EDIF200 Sch Reader Bnd SW	
221496	IC EDIF200 Sch Writer Stn SW	
221499	IC EDIF300/400 Netlist Read Stn SW	
221500	IC EDIF300/400 Netlist Write Stn SW	
221495	IC EDIF300/400 Sch Read Bnd SW	
221497	IC EDIF300/400 Sch Write Stn SW	
30000	Falcon Framework V8 Ap SW	
54769	Schematic Generator V8 Ap SW	
44110	AutoCells Ap SW	LINUX
54743	Back Annotation Op SW	SUN
54744	Clock Tree Op SW	
34689	ICrules Op SW	
34691	ICtrace Op SW	
51688	Pyxis Plot Ap SW	
210433	Pyxis Assemble Op SW	
249131	Pyxis Schematic Ap SW	
249135	Pyxis Implement Stn SW	
202148	Pyxis Digital Devices Op SW	
202149	Pyxis Analog Devices Op SW	
246615	Pyxis Echeck OP Sw	

IE3D		
250838	HL 3D EM_Acceleration Bnd SW	LINUX

Calibre Design Tools		Platform
212210	Calibre PVS Stn SW	
223993	Calibre ADP Op SW	
208309	Calibre CB Ap SW	
226492	Calibre LFD Ap SW	
234967	Calibre YieldAnalyzer Bnd SW	
234968	Calibre YieldEnhancer Bnd SW	LINUX
205003	Calibre MT-2nd CPU Op SW	SUN
221191	Calibre xL Op SW	
210228	Calibre xRC Ap SW	
218002	Calibre xRC CB Ap SW	
223155	Time-it Op SW	
235811	Calibre PERC Ap SW	

Calibre Design Tools		Platform
67851	xCalibrate Ap SW	
210681	Calibre DESIGNrev Ap SW	
242872	Calibre xACT 3D Ap SW	
242873	Calibre xACT 3D Add-On Op SW	
240479	Calibre DRC Auto-Waiver Op SW	
231096	Calibre YieldServer Ap SW	
225431	Calibre xRC to ADMS Op SW	LINUX SUN
248337	Calibre RealTime Ap SW	
241456	Calibre Pattern Match Op SW	
241971	Calibre InRoute Op SW	
248461	Calibre xACT SOC Ap SW	
250155	Calibre Multi-Patterning Op SW	

Calibre Manufacturing Tools		
207041	Calibre LITHOview Ap SW	
205564	Calibre MT-OPCpro Op SW	
240233	Calibre nmDP Op SW	
231422	Calibre nmOPC Op SW	
204413	Calibre OPCpro Op SW	
211566	Calibre OPCsbar Op SW	
225747	Calibre OPCverify Op SW	
203329	Calibre ORC Op SW	
204415	Calibre PSMgate Op SW	
238686	Calibre FRACTUREall Op SW	
226095	Calibre FRACTUREc Op SW	
220546	Calibre FRACTUREh Op SW	
211030	Calibre FRACTUREj Op SW	
209427	Calibre FRACTUREm Op SW	
212329	Calibre FRACTUREt Op SW	
231209	Calibre FRACTUREv Op SW	LINUX
235012	Calibre MASKOPT Op SW	SUN
224372	Calibre MDP Embedded SVRF Op SW	
220547	Calibre MDPmerge Op SW	
210899	Calibre MDPview Ap SW	
231210	Calibre MetrologyAPI Op SW-LSL3	
224371	Calibre MPCPro Op SW	
237747	Calibre nmMPC Op SW	
204416	Calibre WORKbench Ap SW	
242811	Calibre nmOPC DDL Op SW	
242837	Calibre nmSRAF Op SW	
248195	Calibre EUV Op SW	
242817	Calibre Pixbar Op SW	
204414	Calibre PRINTimage Op SW	
211565	Calibre nmBias Op SW	
225771	Calibre ILO Op SW	
250064	Calibre pxOPC Ap SW	

DESIGN VERIFICATION & TEST

HEP's Design, Verification and Test package provides complete solutions for HDL design, verification, synthesis and test of ASICs and FPGAs:

- Questa Advanced Functional Verification Platform – Completely standards based, Questa is the most advanced functional verification product in the industry, supporting assertion based verification, coverage driven verification, testbench automation and formal analysis of clock domain crossing, supported by a comprehensive suite of Verification IP.

- FPGA Design and Verification – A complete solution comprising HDL design, simulation, hardware/software co-verification and leading FPGA logic and physical synthesis.

- C Based Design and Verification – A comprehensive suite of tools for design creation and analysis using C and System C.

- Tessent Silicon Test – A complete technology-leading solution for testability analysis, scan, boundary scan and memory test synthesis, and automatic test pattern generation.

- Hardware-Software Co-Verification – The industry's leading solution for verification of hardware and software.

- System Modeling – A complete environment for creation and verification of mixed-signal and Multilanguage systems, prevalent in automotive electrical systems, control systems and mechatronic systems.

DESIGN VERIFICATION & TEST TOOLS

Design Entry & Synthesis		Platform
252991	HDL Designer Ap SW	
242631	Visual IP-XACT Ap SW	
211639	Precision RTL Synthesis Ap SW	
233858	Precision RTL Plus Ap SW	LINUX
204437	Spectrum Lev 3 ASIC-VHDL Ap SW	SUN
204436	Spectrum Lev 3 ASIC-VLOG Ap SW	WIN
204435	Leonardo Insight Op SW	
204434	Leo Spectrum Lev 3-ASIC Op SW	
204439	XLIB Creator Ap SW	WIN

High-Level Deign		
236144	Vista Architect Stn SW	
232656	Visual SLD Pro Stn SW-MD	
239154	Visual SLD Pro LNL Plus Stn SW	LINUX
239732	Vista HCE Op SW	SUN
235983	ReqTracer Ap SW	WIN

Functional Verification		
224747	Questa AFV Ap SW *(includes ModelSim functionality)*	
248475	Questa Ultra Bnd SW	
241222	Questa Codelink Runtime Ap SW	
241221	Questa Codelink Debugger Op SW	
243168	Questa CDC Ap SW	LINUX
222972	Questa CDC-FX Op SW	SUN
243169	Questa Formal Ap SW	WIN
235831	Questa Verification IP Library Ap SW	
241818	Questa Codelink Turbo Ap SW	
228131	Freescale Family Op SW	
204610	FormalPro V8 Ap SW	
54769	Schematic Generator V8 Ap SW	

Silicon Test		Platform
242156	Tessent MemoryBIST Ap SW	
242160	Tessent BoundaryScan Ap SW	
242174	Tessent SoCScan Ap SW	LINUX
43879	Tessent FastScan Ap SW	SUN
202287	Tessent FastScan MacroTest Op SW	
211099	Tessent TestKompress Ap SW	
225549	Tessent Diagnosis Ap SW	
253473	Tessent IJTAG Ap SW	

System Level Engineering		
230522	BridgePoint UML Suite Ap SW	
235832	BridgePoint MC Exec C Op SW	WIN
251438	SystemVision Integration Ap SW	

PCB AND SYSTEM DESIGN

Mentor Graphics is the market leader in PCB design, implementation & analysis. Our integrated solutions support a complete flow for design definition with schematics & HDL, FPGA integration for reduced design cycles, an integrated layout & routing environment & powerful signal integrity analysis tools.
For HEP's new members, Mentor Graphics has two product offerings in the area of printed circuit board design.

These are Expedition– the most powerful solution targeted at the mid-sized to large organization or for the systems design group with pervasive use of leading edge PCB or high speed technologies and PADS– a complete PCB design solution combining schematic definition with powerful layout & simulation tools.

Included in each of these product lines is HyperLynx, Mentor's powerful signal and power integrity analysis suite. The HyperLynx suite of tools can be used in virtually any design flow to help eliminate signal integrity, crosstalk, & EMC problems early, allowing you to "get it right the first time." These simulation tools come ready to use with unprecedented ease of use, delivering high-speed analysis to every engineer's desktop.

For PCB products that may be available to renewing HEP members, please visit the supplemental brochure.

PCB PADS

PADS is a complete PCB design solution combining schematic definition with powerful layout & simulation tools.

It provides an integrated design environment combining ease of use with functional depth.
With PADS PCB design solutions, you will:

- Achieve a high ROI on PCB designs ranging from basic to complex.
- Improve productivity with shorter design cycles.
- Maintain design integrity with the latest analysis and simulation tools.

PCB PADS TOOLS

PCB Design		Platform			Platform
240146	DxDesigner 040 Ap SW		221154	Eldo for HyperLynx Analog Op SW	
237180	PADS Layout 575 Kit Ap SW	WIN	225414	HyperLynx Analog Op SW	
234536	DxDesigner 076 Bnd SW		233857	HyperLynx Thermal Ap SW	WIN
			236251	EZwave for HyperLynx Ap SW	
			239985	HyperLynx SI GHz Bnd SW	
			243481	LPW. Mentor PADS Ap SW	

PCB EXPEDITION

The Expedition Series flow is tailored for the mid to large-sized organization or the systems design group with pervasive use of leading edge PCB or high speed technologies. This tightly integrated solution composes the industry's most advanced design and analysis functionality in an environment of constraint, library and design data management.

- Most advanced place and route technology for high design productivity, fast time-to-market and performance optimized products.
- High-speed pre and post-layout analysis for classical MHz routes and multi-gigabit serial interconnects.
- Design capability for leading PCB technologies such as HDI/microvia, embedded components, flex and rigid –flex and high pin-count/performance IC packages.
- Tight integration with FPGA design solutions for reduced design cycle time and optimized system performance.
- Common constraint editing and management system feeds all tools in the flow for one-time, easy-to-use entry.
- Patented concurrent team design technology reduces layout design time by 40-70%.

The PCB Expedition package includes:

- Design Capture & System Design - DxDesigner Products
- Board Level Simulation – HyperLynx
- PCB Layout – Expedition Pinnacle, Team PCB • FPGA Integration – I/O Designer
- Manufacturing – CAM Output Manager, Fablink XE Pro • Thermal Simulation – HyperLynx
- Signal Integrity Analysis – HyperLynx GHz, Quiet Expert

PCB EXPEDITION TOOLS

System Design		Platform
221965	Variant Mgr Bnd SW	
220524	EDIF 200 Graphics I/F Op SW	
225311	Electrical CES Ap SW	
219171	I/O Designer Ap SW	
238784	DxDesigner ExpPCB Bnd SW	
206071	Library Manager Ap SW	LINUX WIN
206072	Parts Manager Admin Ap SW	
206073	Parts Manager Client Ap SW	
234107	DxD RF Design Op SW	
245599	PCB Design for Test Op SW	
247571	eDxDesigner Op SW	

Analysis and Verification		
221154	Eldo for HyperLynx Op SW	
225414	HyperLynx Analog Op SW	
239985	HyperLynx SI GHz Bnd SW	
233857	HyperLynx Thermal Ap SW	
220414	QUIET Expert Ap SW	LINUX WIN
236251	EZwave for HyperLynx Ap SW	
239967	HyperLynx PI Power Bnd SW	
239999	HyperLynx SI DDRx Ap SW	
250382	HyperLynx DRC PCB Standard Ap SW	

Physical Design Products		Platform
206143	Advanced Interconnect Op SW	
212132	Exp Design Reuse Op SW	
234108	ATP RF Design Op SW	
206079	Expedition PCB Browser Ap SW	
222402	Expedition PCB Pinnacle Ap SW	
220633	Fablink XE Pro Ap SW	
215981	OrCad schematic Exp IF Op SW	
225192	PCB DFF Analysis Op SW	LINUX
206059	PCB Planner Ap SW	WIN
206060	PCB Viewer Ap SW	
215737	TeamPCB - WG Op SW	
236799	3D PCB Viewer Op SW	
221242	ATP Flex for Expedition PCB Op SW	
227594	ATP Embedded Passives Op SW	
247560	eExpedition Op SW	
243440	LPW. Mentor Expedition Ap SW	

IE3D		
250838	HL 3D EM_Acceleration Bnd SW	WIN

AUTOMOTIVE DESIGN & ANALYSYS

Over the last 20 years the number of mechatronic systems in the typical automobile has grown from an average of four to more than 20 and as many as 80. In response to the need for improved quality, efficiency and functionality of the design-to-manufacture flow while managing costs across the product life cycle, Mentor Graphics' solutions respond to the demanding problems of modern EE design.

Mentor Graphics offers design packages specifically for automotive engineering, comprising:

- Capital Harness Systems (CHS) – Electrical distribution system design, simulation and analysis, design data and change management, engineering and manufacturing analysis and support, enterprise integration (bridges for MCAD, PDM, etc.)

- Volcano™ Communication Technology – Network design automation tools, multiplex bus system analysis (CAN, LIN, FlexRay, etc.), deterministic approach to "correct-by-construction" in-vehicle software, network test and validation.

- SystemVision – A complete environment for creation and verification of mixed-signal and Multilanguage systems, prevalent in automotive electrical systems, control systems and mechatronic systems.

SYSTEM MODELING

SystemVision - A complete environment for creation and verification of mixed-signal and multi-language systems using the power of VHDL-AMS to verify your control and mechatronic systems.

SystemVision targets the following applications:

- Automotive Electrical Systems, addressing challenges such as increased vehicle mechatronic content, dual voltage system architecture design, function design & analysis & communication systems.

- Control Systems, such as system concept feasibility, digital/analog subsystem partitioning, hardware/software/firmware partitioning, subsystem I/O specification & dynamic system interactions not found at the subsystem level.

- Mechatronic Systems Design technology challenges such as mixed-technology systemic interactions, device sizing & component rating & algorithm testing with "software in the loop" capabilities.

CABLING & HARNESS DESIGN

Capital Harness Systems (CHS) - Addresses the complexities of integration and interconnection of electrical systems and design of their associated wire harnesses by providing a common, attractive user environment; a very rich data structure; and infrastructure services such as security and data reporting, and innovative functionality specialized to the electrical domain. This includes electrical distribution system design, simulation and analysis, design data and change management, engineering and manufacturing analysis and support, enterprise integration (bridges for MCAD, PDM, etc.).

Benefits

- Fully integrated application suite for electrical system design, electrical analysis, system integration / wiring design and harness engineering.
- Powerful embedded data management capabilities (vehicle configuration management, design comparison, data sharing etc).
- Productivity enhanced by modern technologies (wiring synthesis, interpretive analysis, diagram synthesis,
- etc).
- Architected for large organizations (multi-user, multi-site) with powerful enterprise integration capabilities.

CABLING & HARNESS TOOLS

Capital Harness		Platform
237294	Capital Integrator Comp Ap SW	LINUX SUN WIN
222413	Capital AVAssist Int Op SW	
223747	Capital Brg Top NX Op SW	
222016	Capital Brg Log NX Op SW	
221957	Capital SimGrid Ap SW	
223381	Capital Ground Design Op SW	
223743	Capital Integration Srv Ap SW	
234971	Capital Ent Reporter Ap SW	
222581	Capital Logic Ap SW	
221984	Capital SimCertify Op SW	
221985	Capital SimProve Op SW	
221986	Capital SimScript Op SW	

Capital Harness		Platform
221960	Capital SimStress Op SW	LINUX SUN WIN
233365	Capital SimBridge Op SW	
237474	Capital HarnessXC Adv Bnd SW	
230930	Capital Brg XC NX Op SW	
240873	Capital Topology Op SW	
239184	Capital Insight Op SW	
Vesys 2.0 Products		
240850	VeSys Electrical Bnd SW	WIN
SystemVision		
251438	SystemVision Integration Ap SW	WIN

VEHICLE NETWORKING

Volcano Network Architect (VNA) is Mentor Graphics' design and analysis tool for CAN and LIN communication systems. VNA provides design, analysis and administrative functions and supports designing systems with legacy electronic control units (ECUs) with fixed messaging. VNA is a standalone tool suitable for integration in legacy design processes as well as the ideal foundation for building a system engineering-based communication design process on. The VNA connects easily to other tools, for example enterprise-wide communication databases.

Volcano Network Architect: High level requirement capturing early in the design, automatically map signals to frames for better bandwidth utilization. HW-independent, signal-based API abstracting.

Volcano In-Vehicle Software: Signals-oriented API simplifies application development by abstracting the communication from the application; predictable behavior reduces testing.

Volcano Test and Validation: Monitor and display multiple network signals in one tool. Advanced emulation capabilities for simulating user-defined functionality.

System Vision: A mixed-signal modeling & simulation environment using the power of VHDL-AMS to verify your control & mechatronic systems.

VEHICLE NETWORKING TOOLS

Vehicle Networking		Platform
225668	Volcano VNA Integrator Seat	
225667	Volcano VNA Data Server Lic	
225670	Volcano LNA Dev Seat License	
237563	Volcano VSA	
234892	Volcano VSB	WIN
239558	Virtual Sys Integrator Ap SW	
237528	Volcano FR Com Designer	
237527	Volcano CAN Com Design	
237529	Volcano LIN Com Design	
SystemVision		
251438	SystemVision Integration Ap SW	WIN

MECHANICAL ANALYSIS

Mentor Graphics Higher Education Program's Mechanical Analysis Package provides complete solutions for electronics thermal design (electronics cooling); building heating and ventilation (HVAC); and Concurrent CFD – Computational Fluid Dynamics software embedded in a mechanical CAD environment.

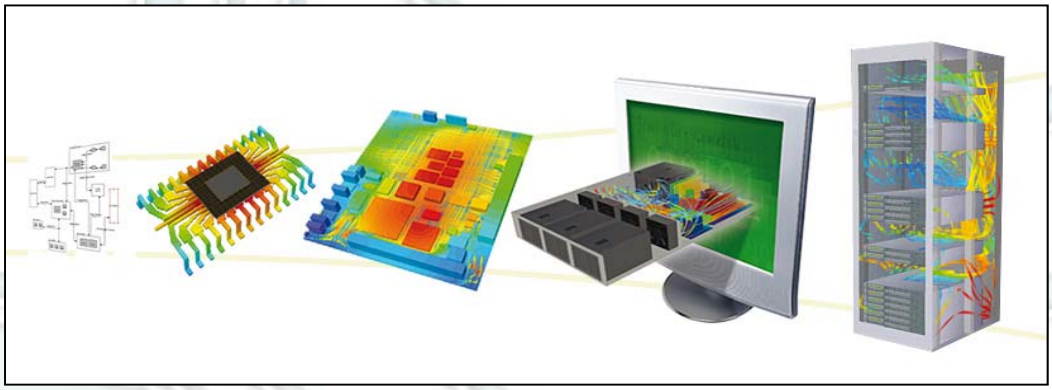
Electronics Thermal Design - Mentor Graphics supports the complete thermal design of electronics from IC package through to system-level cooling.

- **FloTHERM™** - 3D computational fluid dynamics software that predicts airflow and heat transfer in and around electronic equipment, from components up to complete systems.
- **FloTHERM PCB™** - accelerates the conceptual thermal design of printed-circuit boards.

Building Heating & Ventilation - FloVENT™ predicts and optimizes airflow & quality, temperature and contamination control in all types of buildings from data IT rooms, clean rooms, auditoriums, office buildings, car parks, vehicles, laboratories & hospitals.

Concurrent CFD - helps Engineers overcome challenges with Mechanical Engineering projects. **FloEFD™** is a full-featured 3D fluid flow and heat transfer analysis package without the numerical complexity and meshing overhead of traditional CFD.

Flowmaster® is the leading general purpose 1D Computational Fluid Dynamics (CFD) solution for modeling and analysis of fluid mechanics and pipe flow in complex systems early in the development process



MECHANICAL ANALYSIS TOOLS

FloTHERM		Platform
239808	FloTHERM Parallel Ap SW	
239804	FloTHERM Solver Op SW	
239803	FloTHERM 4 CC Solver Op SW	LINUX
239805	FloTHERM Parallel Solver Op SW	SUN
239806	FloTHERM 4 CC Parallel Solver Op SW	WIN
240109	FloTHERM.PCB Ap SW	

FloVENT		Platform
239811	FloVENT Ap SW	
239815	FloVENT Parallel Ap SW	
239817	FloVENT Solver Op SW	SUN
239816	FloVENT 4 CC Solver Op SW	WIN
239818	FloVENT Parallel Solver Op SW	
239819	FloVENT 4 CC Parallel Solver Op SW	

FloMCAD Products		
239822	FloMCAD.Bridge Ap SW	WIN

FloEFD Products		
238141	FloEFD Ap SW	
238154	FloEFD Electronics Cooling Mod Op SW	WIN
238395	FloEFD Adv CFD Module Op SW	

FlowMaster	
254281	FlowMaster Educational Edition

RENEWING MEMBERS ONLY

For our existing members, please make sure that you that you review this section as it contains information on the changes to this year's program and packages available only to members renewing the use of specific tools.

This year, notable modifications to our product collection are as follows:

IC NANOMETER DESIGN

IC Design Flow

- Newly added tools – Pyxis Echeck OP Sw

Analog/Mixed Signal Simulation

- Removed tools – AdiT Rail is no longer available

Physical Verification and Manufacturing

- **Newly added design tools –** Calibre InRoute,

DESIGN, VERIFICATION & TEST

Functional Verification Products

- Certe Testbench Studio is no longer offered

Tessent Silicon Test

- Tessent IJTAG is added to the Program

PCB EXPEDITION

- The **Expedition Renewals Only** package has been obsoleted. The legacy tools included within this package are no longer supported. All functionality is included within the DxDesigner and HyerLynx products which are included within the Expedition package.

RENEWING MEMBERS ONLY

PACKAGES AVAILABLE FOR RENEWING MEMBERS ONLY

The following two packages, are only available to members who are renewing the use of these tools.

PCB BOARD STATION

Components		Platform
34657	AccuParts V8 Op SW	
66919	AccuSim-HDLA Analog Dsn Stn SW	
34625	AutoTherm V8 Ap SW	
203465	Continuum QS Stn SW	
51630	HDL-A/DEV V8 Op SW	
220391	IdeaStationII SW	
34649	QuickFault II Kernel V8 Op SW	
62000	Board Architect Stn V8 SW	
39986	Board Stn V8 SW	LINUX
217332	EDIF 200 Netlist Reader Op SW	SUN
217333	EDIF 200 Netlist Writer Op SW	WIN
218751	EDIF 200 Schm Reader Op SW	
218753	EDIF 200 Schm Writer Op SW	
218752	EDIF 300-400 Schm Reader Op SW	
218755	EDIF 300-400 Schm Writer Op SW	
218756	EDIF300-400 Netlist Reader Op SW	
218757	EDIF300-400 Netlist Writer Op SW	
57119	EngineerView Lite V8 Op SW	
30000	Falcon Framework V8 Ap SW	
51640	Falcon ITK V8 Ap SW	
56401	FlowXpert Ap SW	SUN
56402	XpertBuilder Ap SW	WIN
56403	ProjectXpert Ap SW	

Components		Platform
205653	HDLWrite PLUS V8 Ap SW	
51637	Interactive Layout V8 Ap SW	
204418	IS Synthesizer Ap SW	
204422	IS_Analyzer Ap SW	
204419	IS FloorPlanner Ap SW	
204420	IS Optimizer Ap SW	
204417	IS MultiBoard Op SW	
221331	TAU Ap SW	
57105	ManufactureView V8 Ap SW	
44164	MultiChipMod Dsnr Stn V8 SW	
42732	PCB Mechanical I/F V8 Op SW	LINUX
34635	PCB PACKAGE V8 Ap SW	SUN
34640	ProtoView V8 Ap SW	WIN
51576	PTM:SITE V8 Op SW	
206644	RE AutoRouter V8 Op SW	
206646	RE Network AutoRouter V8 Op SW	
206645	RE HighSpeed V8 Op SW	
67977	RF Architect V8 Op SW	
202131	RF EM Sim Interface Op SW	
67978	RF Layout V8 Op SW	
54769	Schematic Generator V8 Ap SW	
215736	TeamPCB - EN Op SW	

EMBEDDED SW DEVELOPMENT TOOLS

Embedded Software		Platform
230918	EDGE Developer Suite	
	ARM GNU Compiler Tools	
	EDGE SimTest	WIN
	Microtec C Compiler PPC	
	EDGE Debug	

SUPPORT, TRAINING & MEMBERSHIP

SUPPORT

SupportNet, www.mentor.com/supportnet, Mentor's online support, provides the fastest and easiest way to resolve your technical and licensing support issues. On SupportNet, faculty and staff may:

- Access timely, product specific information
- Quickly troubleshoot technical issues
- Easily search documentation
- Securely download the latest releases and patches for all Mentor Graphics products if you are part of the faculty
- Learn, share and network with peers on Mentor Communities
- Suggest and vote on product improvements with Mentor Ideas

SupportNet Registration only takes a few minutes. To register, you will need your standard contact details, your university email address, and your Mentor Graphics site number. Faculty and staff may have full access to SupportNet. Students may register for SupportNet Knowledge Access to use TechNotes, AppNotes and Documentation.

TRAINING

Mentor Graphics offers a wide range of classes and web-based events and is committed to ensuring that members are fully trained in the use of Mentor's tools. In North America and Europe, faculty and teaching staff members are eligible to attend Mentor Graphics' advertised public training classes at zero cost on a space available basis. To view or register for these courses, visit the Education Services Website, www.mentor.com/training_and_services.

For other regions, please contact your local HEP representative.

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Mentor Graphics partners with CMC Microsystems to donate our design tools to CMC member universities.

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Mentor Graphics also partners with EUROPRACTICE to donate tools to Universities.

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